

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/749,052	BOLTON ET AL.	
	Examiner	Art Unit	
	Eric B. Kiss	2192	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-			
B	US-			
C	US-			
D	US-			
E	US-			
F	US-			
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)	
U	Alfred v. Aho, et al., "Compilers: Principles, Techniques, and Tools," 1988, Addison-Wesley, pp. i, ii, 463-473, 585-595.	
V	Rajiv Gupta, et al., "Path Profile Guided Partial Dead Code Elimination Using Predication," 1997, proc. 1997 Int'l Conf. on Parallel Architectures and Compilation Techniques, pp. 102-113.	
W	Jens Knoop, et al, "Partial Dead Code Elimination," 1994, proc. ACM SIGPLAN 1994 conference on Programming language design and implementation, pp. 147-158.	
X	Munehiro Takimoto and Kenichi Harada, "Partial Dead Code Elimination Using Extended Value Graph," 1999, Springer-Verlag, Lecture Notes in Computer Science, vol. 1694, proc. Of the 6th Int'l Symposium on Static Analysis, pp. 179-193.	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a))
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.